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TTL- ja CMOS-piirejä

Taulukko 1: Valikoima TTL-numeroinnin mukaisia 74-alkuisia logiikkapiirejä (englanninkielinen terminologia on data-kirjoissa vakiintuneempaa ja usein ytimekkäämpää kuin suomi). CMOS-piireissä käytetään usein myös samoja 74-alkuisia koodeja.

Tyyppi	Kuvaus	Tyyppi	Kuvaus
7400	quad 2-input NAND	7433	quad 2-input NOR buffer, o. c.
7401	quad 2-input NAND, open collector output	7437	quad 2-input NAND buffer
7402	quad 2-input NOR	7438	quad 2-input NAND buffer, o. c.
7403	quad 2-input NAND	7440	dual 4-input NAND buffer
7404	hex inverter	7442	BCD-to-decimal decoder
7405	hex inverter, open collector output	7445	BCD-to-decimal decoder, o. c.
7406	hex inverter, buffer, o. c.	7446	BCD to 7-segment decoder
7407	hex buffer, o. c.	7447	BCD to 7-segment decoder, o. c.
7408	quad 2-input AND	7448	BCD-to-decimal decoder
7409	quad 2-input AND, open collector output	7449	BCD-to-decimal decoder, o. c.
7410	triple 3-input NAND	7451	dual 2-wide 2-input AND/NOR
7411	triple 3-input AND	7454	4-wide 2-input AND/NOR
7412	triple 3-input NAND, o. c.	7455	2-wide 4-input AND/NOR
7413	dual 4-input NAND Schmitt trigger	7456	50 to 1 frequency divider
7414	hex inv. Schmitt trigger	7457	60 to 1 frequency divider
7415	triple 3-input AND	7473	dual J-K master-slave flip-flop
7416	hex inv. buffer, o. c.	7474	dual D-type pos.-edge-triggered flip-flop
7417	hex buffer, o. c.	7475	4-bit D-type latch
7419	hex inv. Schmitt trigger	7476	dual J-K master-slave flip-flop
7420	dual 4-input NAND	7477	4-bit D-type latch
7421	dual 4-input AND	7483	4-bit full adder
7422	dual 4-input NAND, o. c.	7485	4-bit magnitude comparator
7424	quad 2-input NAND Schmitt trigger	7486	quad 2-input XOR
7425	dual 4-input NOR, strobed	7490	4-bit decade counter
7426	quad 2-input NAND, o. c.	7492	divide-by-12 counter
7427	triple 3-input NOR	7493	4-bit binary counter
7428	quad 2-input NOR buffer	7495	4-bit parallel access shift register
7430	8-input NAND	7496	5-bit shift register
7431	delay		
7432	quad 2-input OR		

Jatkuu monta sivua!

Taulukko 2: Valikoima TTL-numeroinnin mukaisia 74-alkuisia logiikkapiirejä (jatkoa).

Tyyppi	Kuvaus	Tyyppi	Kuvaus
74107	dual J-K master-slave flip-flop	74174	hex D-type flip-flop
74109	dual J-K pos.-edge-triggered flip-flop	74175	quad D-type flip-flop
74112	dual J-K neg.-edge-triggered flip-flop	74180	8-bit odd/even parity generator/checker
74113	dual J-K neg.-edge-triggered flip-flop	74181	4-bit ALU
74114	dual J-K neg.-edge-triggered flip-flop	74182	look ahead carry logic
74121	monostable multivibr.	74183	dual carry save full adder
74122	retriggerable monostab. multivibr.	74189	16 x 4 tri-state RAM, o. c.
74123	dual retr. monost. multiv.	74190	synchronous 4-bit decade up/down counter
74124	dual VCO	74191	synchronous 4-bit decade up/down counter
74125	quad tri-state buffer	74192	synchronous 4-bit decade up/down counter
74126	quad tri-state buffer	74193	synchronous 4-bit decade up/down counter
74128	quad 2-input 50 Ω NOR driver	74194	4-bit bidirectional PIPO shift register
74131	3 to 8 -line decoder/latch	74195	4-bit parallel access shift register
74132	quad 2-input NAND Schmitt trigger	74196	4-bit presettable decade counter/latch
74133	13-input NAND	74197	4-bit presettable binary counter/latch
74136	quad 2-input XOR, o. c.	74221	dual monostab. multivibr.
74137	3 to 8 -line decoder/latch	74237	3 to 8 line decoder/address latch
74138	3 to 8 -line decoder/multiplexer	74238	3 to 8 line decoder/demultiplexer
74139	dual 2 to 4 -line decoder/multiplexer	74240	octal tri-state buffer/driver (inv.)
74143	synchr. BCD counter/latch/decoder/driver	74241	octal tri-state buffer/driver (noninv.)
74145	BCD-to-decimal decoder/driver	74242	quad bus transceiver
74147	10 to 4 -line priority encoder	74243	quad bus transceiver
74148	8 to 3 -line priority encoder	74244	octal tri-state bus driver
74150	16 bit data selector	74245	octal tri-state bus transceiver
74151	8 to 1 -line data selector/multiplexer	74247	BCD to 7-segment decoder
74153	dual 4 to 1 -line data selector/multiplexer	74251	8 to 1 line tri-state data selector/multiplexer
74154	4 to 16 -line decoder/multiplexer	74253	dual 4 to 1 line tri-state data selector/multiplex.
74155	dual 2 to 4 -line decoder/multiplexer	74257	quad 2 to 1 line data selector/multiplexer
74156	dual 2 to 4 -line decoder/multiplexer	74258	quad 2 to 1 line data selector/multiplexer
74157	quad 2 to 1 -line selector/mux	74259	8-bit addressable latch
74158	quad 2 to 1 -line selector/mux	74266	quad 2-input XNOR, o. c.
74159	4 to 16 -line decoder/mux, o. c.	74273	octal D-type flip-flop
74160	synchr. 4-bit decade counter	74276	quad J-K flip-flop (inv.)
74161	synchr. 4-bit binary counter	74279	quad S-R latch
74162	synchr. 4-bit decade counter	74280	9-bit odd/even parity checker/generator
74163	synchr. 4-bit binary counter	74283	4-bit binary full adder
74164	8-bit SIPO shift register	74290	4-bit decade counter
74165	8-bit PISO shift register	74293	4-bit binary counter
74166	8-bit PISO shift register	74294	programmable frequency divider
74169	synchr. 4-bit binary up/down counter	74295	4-bit shift register
74170	4 x 4 register file	74298	quad 2-input multiplexer with storage
74173	quad tri-state D-type flip-flop	74299	8-bit universal shift register
		74321	crystal controlled oscillator
		74322	8-bit shift register
		74323	8-bit PIPO shift register

Taulukko 3: Valikoima TTL-numeroinnin mukaisia 74-alkuisia logiikkapiirejä (jatkoa).

Tyyppi	Kuvaus	Tyyppi	Kuvaus
74348	8 to 3 -line tri-state priority encoder	74603	memory refresh controller
74352	dual 4 to 1 line data selector/multipl. (inv.)	74620	octal bus XCVR (inv.)
74353	dual 4 to 1 l. tri-st. data select./multipl. (inv.)	74621	octal bus XCVR (noninv.)
74354	8 to 1 line data selector/multiplexer register	74623	octal bus XCVR (noninv.)
74356	8 to 1 line data selector/multiplexer register	74624	single VCO
74365	hex bus driver (noninv.)	74628	single VCO, improved TC
74366	hex bus driver (inv.)	74629	dual VCO
74367	hex bus driver (noninv.)	74640	octal bus transceiver (inv.)
74368	hex bus driver (inv.)	74641	octal bus transceiver (noninv.), o. c.
74373	octal D-type transparent latch	74645	octal tri-state bus transceiver (noninv.)
74374	octal D-type flip-flop	74646	octal bus transceiver with register
74375	quad D-type latch	74648	octal bus transceiver with register
74377	octal D-type flip-flop	74651	octal bus transceiver with register
74378	hex D-type flip-flop	74652	octal bus transceiver with register
74379	quad D-type flip-flop	74669	synchronous 4-bit binary up/down counter
74382	4-bit ALU/function generator	74670	tri-state 4 x 4 register file
74385	quad adder/subtractor	74673	16-bit SISO shift register
74386	quad 2-input XOR	74674	16-bit PISO shift register
74390	dual decade counter	74682	8-bit comparator
74393	dual 4-bit decade and binary counter	74684	8-bit comparator
74395	4-bit cascadable tri-state shift register	74688	8-bit magnitude comparator
74396	octal storage register	74690	decade counter register
74399	quad 2-input multiplexer/register	74697	up/down 4-bit binary counter/register
74423	dual retriggerable monost. multivibr.	74699	up/down 4-bit binary counter/register
74445	BCD-to-decimal decoder/driver	74843	9-bit transparent latch
74446	quad bus XCVR (inv.)	74912	6-digit BCD display controller/driver
74449	quad bus XCVR (noninv.)	74915	7-segment to BCD decoder
74465	octal buffer (noninv.)	74922	16-key encoder
74466	octal buffer (inv.)	74923	20-key encoder
74521	octal comparator	74925	4-dig. cntr with multipl. 7-segm. outp. drv.
74533	octal tri-state D-type transparent latch	74928	3½-dig. cntr with multipl. 7-s. outp. drv.
74534	octal tri-state D-type flip-flop	747240	octal bus buffer (inv.)
74538	1-of-8-line tri-state decoder	747241	octal bus buffer
74540	octal bus driver (inv.)	747244	octal bus buffer
74541	octal bus driver (noninv.)	747266	quad exclusive NOR gate
74563	octal D-type transparent latch	747292	programmable divider/timer
74564	octal D-type flip-flop	747294	programmable divider/timer
74573	octal tri-state D-type latch	747640	octal bus transceiver (inv.)
74574	octal tri-st. D-type pos.-edge-triggered f. -f.	747643	octal bus transceiver (noninv.)
74589	8-bit tri-state shift reg. with inp. latches	747645	octal bus transceiver (noninv.)
74590	8-bit tri-state binary counter/output register	7416244	16-bit tri-state buffers/drivers
74592	8-bit binary counter with input register	7416245	16-bit tri-state bus transceivers
74593	8-bit binary counter with input register	7416373	16-bit tri-state transparent D-type latches
74595	8-bit shift register with output latch	7416374	16-bit tri-state edge-triggered D-type f.-f.
74597	8-bit shift register with input latch	7416470	16-bit tri-state transceivers
		7416573	16-bit tri-state transceivers

Taulukko 4: Valikoima RCA:n numeroinnin mukaisia 4-alkuisia CMOS-piirejä.

Tyyppi	Kuvaus	Tyyppi	Kuvaus
4000	dual 3-input NOR + inverter	4045	21-stage counter
4001	quad 2-input NOR	4046	phase-locked loop (vaihelukittu silmukka)
4002	dual 4-input NOR	4047	monostable/astable multivibr.
4006	4,5,8,9,10,12,13,14,16,17 or 18-st. shift reg.	4048	multifunct. expandable tri-state 8-input gate
4007	dual CMOSFET pair + inverter	4049	hex inverter, buffer
4008	4-bit binary full adder	4050	hex buffer (noninv.)
4009	hex inverter, buffer	4051	8-channel analog multiplexer/demultipl.
4010	hex buffer	4052	dual 4-channel analog multiplexer/demultipl.
4011	quad 2-input NAND	4053	triple 2-channel analog multiplexer
4012	dual 4-input NAND	4054	4-line liquid-crystal display driver
4013	dual D-type flip-flop	4055	BCD to 7-segment LC display driver
4014	8-stage shift register	4056	BCD to 7-segment LC display driver
4015	dual 4-stage shift register	4059	programmable divider
4016	quad bilateral switch	4060	14-stage binary counter/divider and osc.
4017	5-stage Johnson decade counter/divider	4063	4-bit magnitude comparator
4018	presettable divide-by-N counter (N = 2-10)	4066	quad bilateral switch
4019	quad 2-input multiplexer (4PDT)	4067	16-channel analog multiplexer/demultipl.
4020	14-stage binary counter	4068	8-input NAND
4021	8-bit PISO shift register	4069	hex inverter
4022	divide-by-8 cntnr/divider with 8 dec. outputs	4070	quad XOR
4023	triple 3-input NAND	4071	quad 2-input OR
4024	7-stage binary counter	4072	dual 4-input OR
4025	triple 3-input NOR	4073	triple 3-input AND
4026	synchr. decade counter with 7-segm. output	4075	triple 3-input OR
4027	dual J-K flip-flop	4076	quad tri-state D-type register
4028	1-of-10 decoder	4077	quad XNOR
4029	synchr. presettable up/down bin./dec. cntnr	4078	8-input OR/NOR
4030	quad XOR	4081	quad 2-input AND
4031	64-stage shift register	4082	dual 4-input AND
4032	triple serial adder	4085	dual 2-wide 2-input AND-OR invert gate
4033	synchr. dec. cntnr with 7-segment output	4086	4-wide 2-input AND-OR invert gate
4034	8-bit bidirectional bus register	4089	Binary rate multiplier
4035	4-bit PIPO shift register	4093	quad 2-input NAND Schmitt trigger
4036	4-word by 8-bit static RAM	4094	8-stage shift-and-store bus register
4038	triple serial adder	4095	gated J-K master-sl. f.-f. (noninv. J,K)
4040	12-stage binary counter	4096	gated J-K master-sl. f.-f. (inv. & noninv. J,K)
4041	quad TTL-buffer (inv./noninv.)	4097	8-channel analog multiplexer/demultipl.
4042	quad D-latch	4098	dual monostab. multivibr.
4043	quad tri-state RS-latch (NOR)	4099	8-bit addressable latch
4044	quad tri-state RS-latch (NAND)		

Taulukko 5: Valikoima 4-alkuisia CMOS-piirejä (jatkoa).

Tyyppi	Kuvaus	Tyyppi	Kuvaus
4104	quad tri-st. low voltage to high voltage trans.	4560	N BCD adder
4192	decade up/down counter	4561	9's complemter
4193	binary up/down counter	4562	128-bit static shift register
4316	quad bilateral switch	4566	industrial time base generator
4351	8-ch. analog multipl./demultipl. with latch	4568	frequency synthesizer
4501	triple: dual 4-inp. NAND, 2-inp. (N)AND	4569	frequency synthesizer
4502	hex strobed inverter, buffer	4572	hex: quad inv., 2-inp. NAND, 2-inp. NOR
4503	hex buffer, tri-state	4583	dual Schmitt trigger
4508	dual 4-bit latch, tri-state	4584	hex Schmitt trigger
4510	presettable BCD up/down counter	4585	4-bit magnitude comparator
4511	BCD to 7-segment latch decoder driver	4724	8-bit addressable latch
4512	8-input tri-state multiplexer	4731	quad 64-bit shift register
4513	BCD to 7-segment latch/decoder/driver	4737	quad decade counter
4514	4-bit latch, 4- to 16-line decoder	4750	frequency synthesizer
4515	4-bit latch, 4- to 16-line decoder	40097	tri-state hex buffer (noninv.)
4516	synchr. presettable binary up/down cntr	40098	tri-state hex buffer (inv.)
4517	dual 64-bit static shift register	40101	9-bit parity generator/checker
4518	dual synchr. decade up counter	40102	8-bit presettable BCD down counter
4519	quad 2-input multiplexer	40103	8-bit presettable binary down counter
4520	dual synchr. 4-bit. binary up counter	40104	4-bit bidirectional shift register
4521	24-stage frequency divider	40105	4-bit x 16 word FIFO register
4522	progr. divide-by-N 4-bit BCD counter	40106	hex Schmitt trigger (inv.)
4526	progr. divide-by-N 4-bit binary counter	40107	dual 2-input NAND buffer/driver
4527	BCD rate multiplier	40109	quad low-to-high voltage level shifter
4528	dual retr. resettable monostable multivibr.	40117	programmable dual 4-bit terminator
4529	dual 4-channel analog data selector	40160	progr. counter with asynchronous clear
4530	dual 5-input majority logic gate	40161	4-bit progr synch bin cntr with asynch clear
4531	12-bit parity tree	40162	4-bit progr synch dec cntr with asynch clear
4532	8-bit priority encoder	40163	4-bit progr synch bin cntr with asynch clear
4534	real-time 5-decade counter	40174	hex D-type flip-flop
4536	programmable timer	40175	quad D-type flip-flop
4538	dual precision monostable multivibrator	40192	4-bit presettable up/down synchr dec cntr
4539	dual 4-channel multiplexer	40193	4-bit presettable up/down synchr bin cntr
4541	oscillator timer	40194	4-bit bidirectional universal shift register
4543	BCD to 7-segm. latch/decoder/drvr (LCD)	40257	quad 2-to-1-line data selector/multiplexer
4551	quad 2-input analog multiplexer		
4553	3-digit BCD-counter		
4555	dual 1-of-4 decoder		
4556	dual 1-of-4 decoder (inv.)		
4558	BCD to 7-segment decoder		